

# 3. Digital Logic

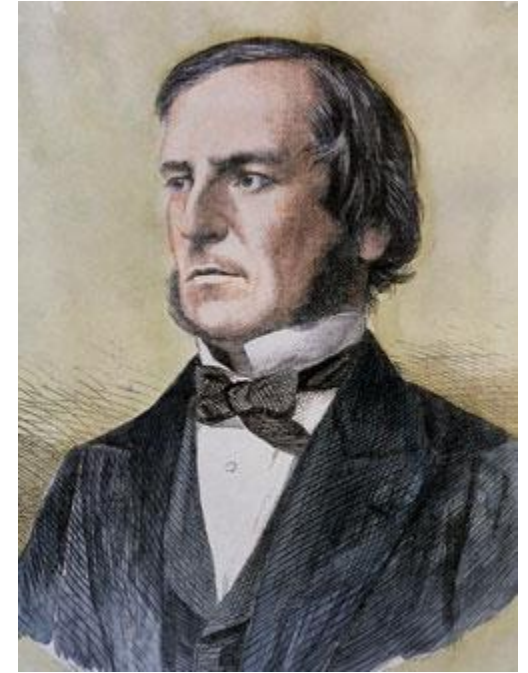
## Chapter 11

### Boolean Algebra and Gates

# Sections 11.1, 11.2

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- Reading: pp.365-370
- Good Problems to Work: 11.1 a., 11.3 a.



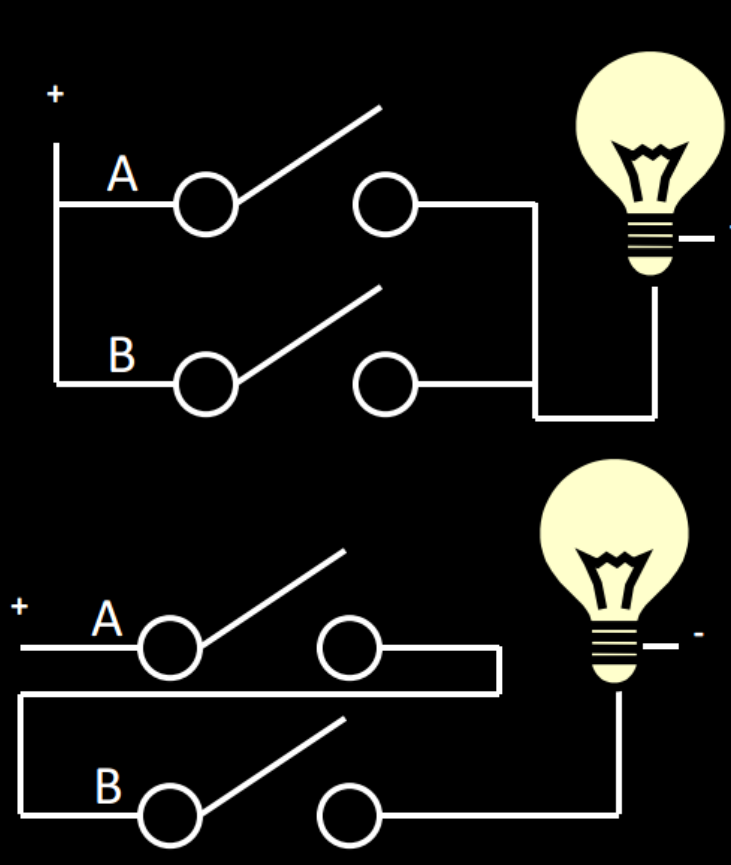
# BOOLEAN ALGEBRA

# Boolean Algebra

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- Boolean algebra is a convenient tool for:
  1. describing the function of digital circuitry
  2. simplifying the implementation of said function
- Boolean algebra uses
  1. logical variables (values are 0 or 1)
  2. operations on logical variables

# AND, OR



The image shows two circuit diagrams on a black background. The top diagram, labeled 'Either (OR)', shows a light bulb connected to a positive terminal (+) through two parallel branches. The first branch contains switch A, and the second branch contains switch B. The bottom diagram, labeled 'Both (AND)', shows a light bulb connected to a positive terminal (+) through two switches, A and B, connected in series. In both diagrams, the light bulb is shown as illuminated.

### Either (OR)

Truth Table

A	B	Light
OFF	OFF	
OFF	ON	
ON	OFF	
ON	ON	

### Both (AND)

A	B	Light
OFF	OFF	
OFF	ON	
ON	OFF	
ON	ON	

# Boolean Operators of Two Input Variables

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<b>P</b>	<b>Q</b>	<b>NOT P</b> ( $\bar{P}$ )	<b>P AND Q</b> ( $P \cdot Q$ )	<b>P OR Q</b> ( $P + Q$ )	<b>P NAND Q</b> ( $\overline{P \cdot Q}$ )	<b>P NOR Q</b> ( $\overline{P + Q}$ )	<b>P XOR Q</b> ( $P \oplus Q$ )
0	0	1	0	0	1	1	0
0	1	1	0	1	1	0	1
1	0	0	0	1	1	0	1
1	1	0	1	1	0	0	0

Truth table of each logical operation

# Logical Operators

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- AND can be  $A * B$ ,  $A \cdot B$ , or  $AB$
- OR is  $A + B$
- NOT can be  $A'$  or  $\bar{A}$
  
- Note: In the absence of parentheses, AND takes precedence over OR

# Boolean Operators of $n$ Input Variables

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Operation	Expression	Output = 1 if
AND	$A \cdot B \cdot \dots$	All of the set $\{A, B, \dots\}$ are 1.
OR	$A + B + \dots$	Any of the set $\{A, B, \dots\}$ are 1.
NAND	$\overline{A \cdot B \cdot K}$	Any of the set $\{A, B, \dots\}$ are 0.
NOR	$\overline{A + B + K}$	All of the set $\{A, B, \dots\}$ are 0.
XOR	$A \oplus B \oplus \dots$	The set $\{A, B, \dots\}$ contains an odd number of ones.



# Identities of Boolean Algebra

## Basic Postulates

$$A \cdot B = B \cdot A$$

$$A + B = B + A$$

Commutative Laws

$$A \cdot (B + C) = (A \cdot B) + (A \cdot C)$$

$$A + (B \cdot C) = (A + B) \cdot (A + C)$$

Distributive Laws

$$1 \cdot A = A$$

$$0 + A = A$$

Identity Elements

$$A \cdot \bar{A} = 0$$

$$A + \bar{A} = 1$$

Inverse Elements

## Other Identities

$$0 \cdot A = 0$$

$$1 + A = 1$$

$$A \cdot A = A$$

$$A + A = A$$

$$A \cdot (B \cdot C) = (A \cdot B) \cdot C$$

$$A + (B + C) = (A + B) + C$$

Associative Laws

$$\overline{A \cdot B} = \bar{A} + \bar{B}$$

$$\overline{A + B} = \bar{A} \cdot \bar{B}$$

DeMorgan's Theorem

# Problem

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1. Prove  $A + 1 = 1$
2. Prove  $A + AB = A$
3. Prove  $A + AB = A$  using perfect induction (complete truth table)

# Problem

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- The complement of any Boolean expression can be found using DeMorgan's Theorem
  1. Find the complement of  $X + (YZ)$
  2. Using a truth table, show you really found the complement


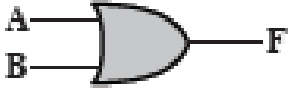


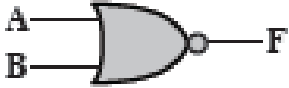
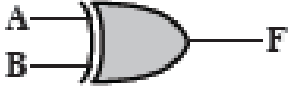
# GATES

# Gates

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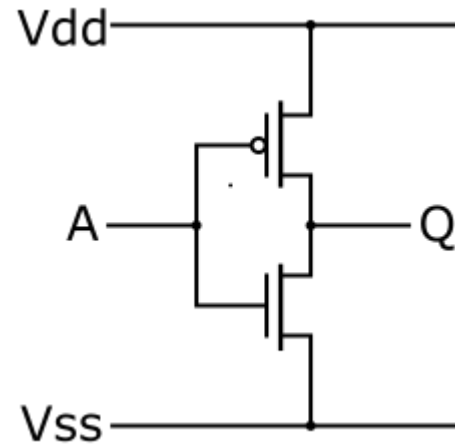
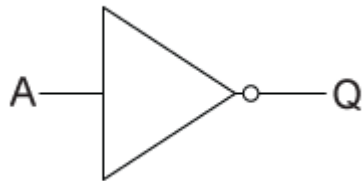
- Gate
  1. an electronic circuit that operates on one or more signals producing an output signal
  2. fundamental building block of ALL digital logic circuits
- Interconnections of these gates allow us to build simple or complicated logical functions

# Basic Logic Gates

Name	Graphical Symbol	Algebraic Function	Truth Table															
AND		$F = A \cdot B$ or $F = AB$	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>F</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> </tr> </tbody> </table>	A	B	F	0	0	0	0	1	0	1	0	0	1	1	1
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0	1	0																
1	0	0																
1	1	1																
OR		$F = A + B$	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>F</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> </tr> </tbody> </table>	A	B	F	0	0	0	0	1	1	1	0	1	1	1	1
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0	1	1																
1	0	1																
1	1	1																
NOT		$F = \bar{A}$ or $F = A'$	<table border="1"> <thead> <tr> <th>A</th> <th>F</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> </tr> </tbody> </table>	A	F	0	1	1	0									
A	F																	
0	1																	
1	0																	
NAND		$F = \overline{AB}$	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>F</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table>	A	B	F	0	0	1	0	1	1	1	0	1	1	1	0
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NOR		$F = \overline{A + B}$	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>F</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table>	A	B	F	0	0	1	0	1	0	1	0	0	1	1	0
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# Gate as Transistor

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# Transistors and Gates

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- Interested in how to build logic gates from transistors?
  - Watch this:  
<https://www.youtube.com/watch?v=NWhk9qGn8qw>



# Next Time

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- Read section 11.3: pp. 370-380