

1 Topics:
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3 Chapter 1-6, 8
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5 OS design goals
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7 Dual Mode
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9 OS Services
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11 OS Structure
12 ASM vs C in the kernel
13 monolithic, microkernel, modular
14
15 Processes
16 PCB
17 What is in a process? Memory regions?
18 How is an ELF related to a process?
19
20 What states can a process be in (for scheduling)?
21
22 What happens in a context switch?
23 fork()/exec() (How does Fork/exec interact with the PCB?)
24 dup2() (How does dup2 interact with the PCB?)
25 pipe()
26 shared memory
27
28 How does your CS460_Shell work?
29
30 Threads
31 how is a thread and process different? The same?
32
33 Benefits?
34 Risks?
35 Define thread-safe
36
37 mutexes
38 semaphores
39 atomic instructions
40
41 multicore vs multi-CPU
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43 Synchronization
44 race condition/critical section
45 3 properties of synchronization
46
47 why is hardware support necessary for synchronization?
48
49 dangers of synchronization
50
51 Scheduling
52 what are the measurable goals of a scheduler?
53 FCFS, SJF, SFJ (preemptive), RR
54 How do you predict the length of the next CPU burst?
55 Multilevel queue scheduling
56
57
58 Main Memory
59 memory heirarchy (fastest to slowest)
60 logical vs physical address
61 what is the memory management unit?
62 first fit/best fit/worst fit

63 fragmentation
64 how does a function in a dynamically loaded shared library get called?
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