

CS460 Review Sheet

Definition of OS.
(Human) User's goals
System's goals
Startup Sequence

Interrupts
 hardware
 software traps
 uses?
 Interrupt vector?

Single processor
multi processor
 why?
 SMP
 AMP
MultiCore
cluster (how different from *MP?)

multitasking
 Pre-emptive MT?

Process
 memory map
 data structures?

Dual Mode
 Kernel
 User

System Calls -- API
 types?
 Examples?
 Passing data?

ASM vs C
Microkernel/layers/monolithic

VM

Process

 ELF
 state machine
 data structures

 PCB
Schedulers
 Job

CPU

Context Switching
 why
 how
 constraints

fork()
exec()

Shared Memory

Message passing

RPC
Java RMI

dup2()
pipe()

Homework Assignment 10 pts

By **9:45 am March 9** write two possible exam questions (and their answers) on the Messageboards (in the CS 460 Lecture Discussion forum). Make sure your question does not duplicate an existing question or the topic of an existing question.